RISCEE 3 Architecture

Clock = load value into register

2 → ADD1

0

1

ALUop
1 X+0
2 X-Y
3 0+Y
4 0
5 X+Y

OR gate

P0

BZ → AND gate

NOT

PC

Read address

Instruction[7-0]

Instruction [15-0]

Instruction Memory

Read Data

Accumulator

RegWrite

Write Data

Y   Zero

ALU3

X

address

Write Data

Read Data

MemWrite

RegDst → 3 2 1 0

ADD2

Clock